



Shaoxing Yuli Semiconductor CO., LTD

绍兴宇力半导体有限公司



U1001A Data Sheet

V 1.3

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Independent Single-Channel Gate Driver

General Description

The U1001A is a single channel MOSFET and IGBT driver with dependent low side referenced output channels. Proprietary HVIC and latch immune CMOS technologies enable ruggedized monolithic construction. The logic input is compatible with standard MOS or LSTTL output, down to 3.3V logic. Internal integrated input logic signal processing circuit, SD fast shutdown processing circuit and high current output driver circuit. The 1001A can be used to drive an N-channel power MOSFET or IGBT which operates up to 30 volts.

Key Features

- Built IN SD Function
- Gate drive supply range from 5 to 30V
- Undervoltage lockout
- 3.3V, 5V input logic compatible
- output in phase with input

Applications

- General Purpose Gate Driver
- DC-DC converters
- Plasma display panel (PDP) applications

Product Summary

V _{OFFSET}	30V max
I _{O+/-}	2.0 A / 2.5A
V _{CC}	5V - 30V
Work Tem	-40 ~125°C

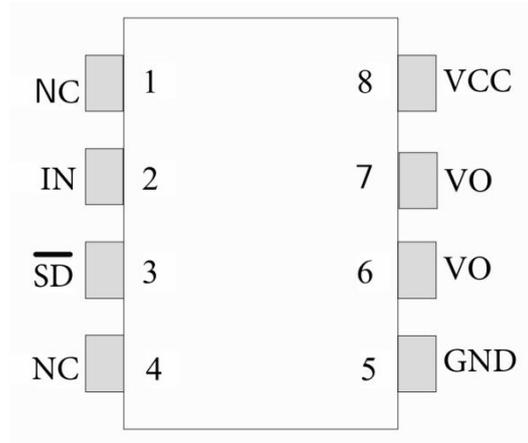
Packages



Products Information

Base Part Number	Package Type	Standard OUT		V _{OFFSET}	Logic Control
		IO+	IO-		
U1001A	SOP8	2.0A	2.5A	30V	IN & \overline{SD}

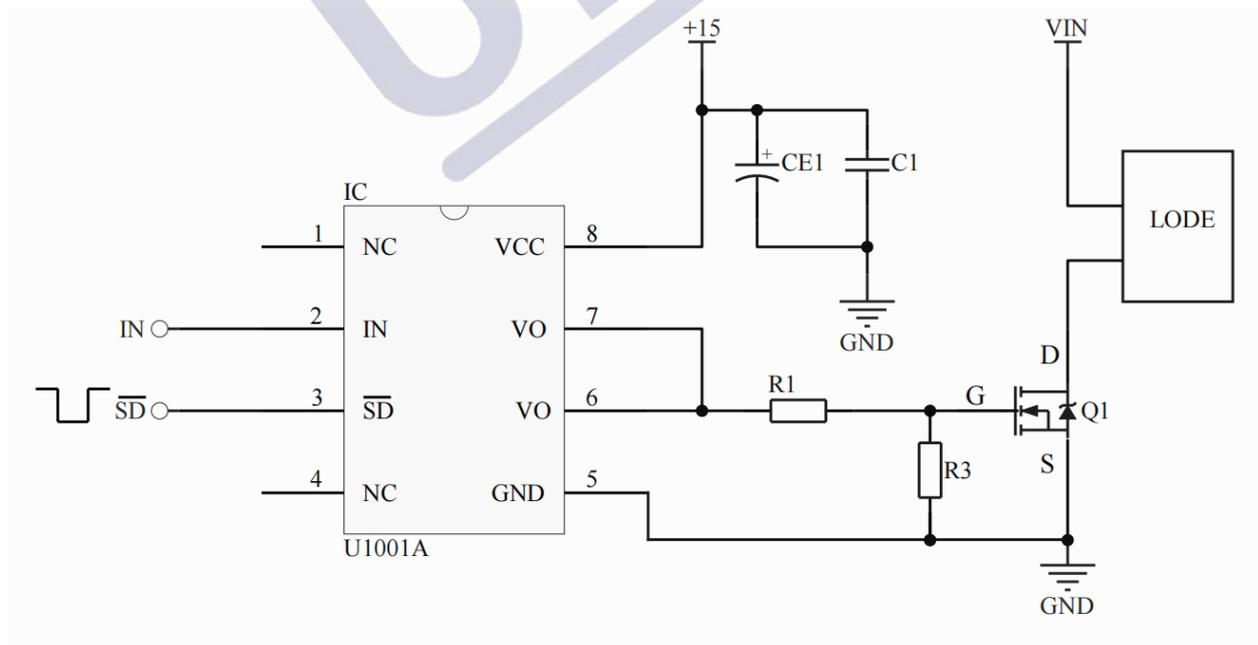
Pin Assignments



Pin Function

Number	Symbol	Description
1, 4	NC	Suggested overhang
2	IN	Logic input
3	\overline{SD}	Logic input for shutdown
5	GND	Ground
6, 7	VO	Gate drive output
8	VCC	Power supply

Typical Application



1、Version Record

Date	Rev.	Description
2018/04/09	1.0	First Release
2020/05/21	1.1	Layout Adjustment
2023/03/18	1.2	Parameter Adjustment
2024/04/26	1.3	Optimize Logic Diagram and Truth Table

2、Contact

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